Ref #	Hits	Search Query	DBs	Default Operator	Plurais	Time Stamp
Li	71	test\$3 and (word\$1 same memory under test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:45
L2	173	repair strategy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:25
L3	1839	spare memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:25
L6	2	i1 and I2 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:40
L7	1034	built-in self-test circuit or "BIST circuit"	US-PGPUB; USPAT; USOGR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:41
L8	106	built-in self-repair circuit or "BISRA"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:50
L10	805007	row or column defect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:44
L11	2890	words test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:44

L12	819	l10 and l11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:44
L13	4	11 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:45
L14	1	113 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:45
L15	1	I13 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:45
L16	398	test\$3 and (memory under test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:46
L17	1	116 and 17 and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:46
L18	15	17 and 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:41
L19	398	(memory under test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:46

L20	1	118 and 119	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 13:47
L21	2	12 and 118	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:41
L22	11	l10 and l18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:42
L23	9	l22 and (l1 or l2 or l3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:53
L24	8612	714/746 or 714/733 or 714/734 or 714/718 or 714/700 or 714/724 or 714/746 or 714/726 or 714/30 or 714/37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:50
L25	77	built-in self-repair analyzer circuit or "BISRA"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:50
L26	1	l23 and l25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:51
L27	2	I23 and I24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:53

L28	85	I23 or I25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/18 14:51
L29	2	I28 and I24	US-PGPUB; USPAT; USOCR; EPO; JPO;	ADJ	ON	2006/05/18 14:51
			DERWENT; IBM_TDB			

5/18/06 3:07:29 PM C:\Documents and Settings\PChung\My Documents\EAST\Workspaces\10822724.wsp Page 4